

ABSTRACT

A graphics controller providing flexible access to a graphics display device by a host.

The controller includes an input bus for coupling to an output bus of the host, an output bus for coupling to the input bus of the graphics display device, a video processing circuit having an

- 5 input coupled to the input bus of the graphics controller and an output coupled to the output bus of the graphics controller, and a bypass switching circuit adapted to switchably couple the input bus of the graphics controller to the output bus of the graphics controller so as to bypass the video processing circuit.